<u>Printed Circuit Board (PCB) First Article (FA) Report</u>

0
0
0
0
0

S/N	Description	CHECK
1	Certificate of Compliance	√
2	Final Product Inspection Report	√
3	Hole Size & Outline Dimension Report	√
4	Micro-section Report	√
5	Solderability Test Report	√
6	Thermal Stress Test Report	√
7	E-Test Report	√
8	Ionic Contamination Report	√
9	RoHS Compliance Statement	√

Final Disposal:	Accept •	Reject	
Checked By:		Date:	
Approved By:		Date:	

Certificate of Compliance

Customer Name:					_
Customer P/N:					_
PO Number:					
Product No.:					
Shipped QTY:					
QTY (pcs):			. <u> </u>	1	_
Date Code:			. <u></u>	1	
	Certifica	ntion Items			
This certificate is to serve a maintained in fabrication of th	-	-	customer	's specifica	ntion are
1 All PCBs shipped comply wi	th purchasing	specifications and	or drawi	ngs.	
2 Where no spec in the drawin	gs and/or mat	terials are given, IF	PC specs a	pply.	
3 Laminate and prepreg used	in fabrication	are in compliance	with IPC-	-4101.	
4 All finish boards have been 1	00% electrica	illy tested and pass	sed.		
5 Finish boards meet all require	rements of UL	. 796 and flammabi	ility rating	g 94V0.	
6 Non-operational printed circ	cuit board and	packing material	should be	discarded	l
according to local laws					
All information listed on this d	locument is he	ereby certified in w	riting to k	oe true.	
A disconding		Date:			
Authorized Signature:		Date.			
Printed Name:		Title:			
		 Final Quality Assura	ance Superv	risor	
Quality Department Stamp			•		

Final Product Inspection Report

Country of Origin: CHINA		Cust PN: 0	Drawing No.: 0					
Produc	t No.: 0		Lot Size: 0	AQL LEVEL II 0.40				
			Flammability Grade: 94V0	Sample size:				
Item D	escription		Specification	Actual	Acc	Rej		
(A) Visual			AQL II 0.40	YES	√			
(B) Materi	al		110211	120	, , , , , , , , , , , , , , , , , , ,	1		
<u> </u>	aw Material				√			
2 C	ore Thickness				√			
	opper Thickness (OZ)				√			
	ng & Circuitry		•		•	•		
	lin Line Width				√			
2 M	lin Spacing				√			
i	ront & Back registration	n			√			
•	e treatment thickness							
`	rocess			/	/			
	AL		/	/	/			
3 le	ad-Free HASL		/	/	/			
4 1-		Ni			/			
4 Imi	nmersion Gold	Au			/			
5 In	nmersion Silver	•	/	/	/			
6 Ir	nmersion Tin		/	/	/			
7 0	old Finger	Ni	/	/	/			
7 G	old Finger	Au	/	/	/			
8 O	SP	•	/	/	/			
(E) Solderi	mask & Component Ma	rking						
1 S/	M Material & Color							
A	pplied on							
2 S/	M Tape Test/Solvent Te	st						
3 S/	M Registration							
	/M Material (Color)							
	pplied on							
	/M Tape Test / Solvent T	Test						
	/M Registration							
(F) Profile	<u> </u>							
1 0	verall Thickness							
2 B	Sow & Twist (max)							
(G) Functi	onal Test							
<u>`</u>	lectrical Test		No Open/Short	YES	√			

	1 1		l	ı						ı	I	I					
(H)	Hole Size & C	Dutline Dimensi	on Repor	rt													
	shed Hole Size(U		_														
1,11112	siled Hole Size(C) IIII. IIIIII)															
Code	Qty	Requirement	Actual	Acc/ Rej	Remarks	Code	Qty	Req	uirement	Actual	Acc/ Rej	Remarks					
1																	
3																	
4																	
5																	
Dime	ension Measure	ment(Unit:inch)				Dime	ension Me	<u>l</u> easuren	nent(Unit:1	 mm)							
No.	Requirement	Tolerance	Actı	ual	Acc/ Rej	No.	Require		Tolerance	Acı	tual	Acc/ Rej					
1					ACC		1					3					
2					ACC												0.3048
3					ACC												
4					ACC												
5					ACC												-
7					ACC ACC												
8					ACC												
9					ACC												
10					ACC												
11					ACC												
12					ACC	+											
13 14					ACC ACC												
15					ACC												
16					ACC												
17					ACC												
18					ACC												
19					ACC ACC												
20					ACC												
22					ACC												
23					ACC												
24					ACC												
25					ACC												
26 27					ACC ACC	+											
28					ACC												
29					ACC												
30					ACC												
31					ACC												
32					ACC	\vdash											
33					ACC ACC												
35					ACC												
36					ACC												
						\vdash											
					+												
V-cu	t measure (Unit	:mm)				Beve	ling meas	sure (U	nit:mm)								
		Requirement	Actu	ıal	Acc/Rej		Items	1	uirement	Act	ual	Acc/Rej			1		
1	Angle				ACC	2	Angle		1		/	/					
	Thickness				ACC		Deepness		/		/	/					

Produ Rer Hole Wall I Request No. 1 2 Result Blind Hole Request No. 1 2 Result Buried Ho Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic	e wall A Ole wa A Indickne	B copper B ll coppe	Mini C Data(U) Minin C er Data(Minin	nit:µm) num: / D (Unit:µm) num: / D	0 0) E ACC	F	+	Average Average:
Request No. 1 2 Result Slind Hole Request No. 1 2 Result Suried Ho Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic	e wall A Ole wa A / ole wa	B Copper B B Copper B	Mini C Data(U) Minin C er Data(Minin	nit:µm) nit:µm) num: / D (Unit:µn num: / D) E ACC	F	Average:/ minimum 0.0 Average: / minimum 0.0 Average: minimum	Average Average:
Request No. 1 2 Result Blind Hole Request No. 1 2 Result Buried Ho Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic Soldermas	A e wall A ole wa A / ole wa	B Copper B B Copper B	Mini C Data(U) Minin C er Data(Minin	nit:µm) num: / D (Unit:µn num: /	E E / m)	F	Average: minimum 0.0 Average: minimum Average: minimum	Average:
Request No. 1 2 Result Slind Hole Request No. 1 2 Result Suried Ho Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic Soldermasl	A Wall A / / Dle wa A / / nickne	B copper B II coppe	Mini C Data(U) Minin C er Data(Minin	nit:µm) num: / D (Unit:µn num: /	E E / m)	F	Average: minimum 0.0 Average: minimum Average: minimum	Average:
No. 1 2 Result Blind Hole Request No. 1 2 Result Buried Ho Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic	e wall A / Dele wa A / / nickne	Copper B Il coppe B Ess Data	Data(Un Minin C er Data(Minin C	nit:µm) num: / D (Unit:µn num: /	ACC E / m)	F	Average: minimum 0.0 Average: minimum Average: minimum	Average:
Result Blind Hole Request No. 1 2 Result Buried Ho Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic	e wall A / Dele wa A / / nickne	Copper B Il coppe B Ess Data	Data(Un Minin C er Data(Minin C	nit:µm) num: / D (Unit:µm num: / D	ACC E / m)	F	Average: // minimum - 0.0 Average: minimum	Average:
Result Slind Hole Request No. 1 2 Result Suried Ho Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic Soldermas	A / / / / Dle wa A / / / / nickne	B II coppe	Minin C Pr Data(Minin C	nit:µm) num: / D (Unit:µn num: / D	E / m)		Average: / minimum - 0.0 Average: minimum	Average:
Result Blind Hole Request No. 1 2 Result Buried Ho Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic	A / / / / Dle wa A / / / / nickne	B II coppe	Minin C Pr Data(Minin C	nit:µm) num: / D (Unit:µn num: / D	E / m)		Average: / minimum - 0.0 Average: minimum	Average:
Request No. 1 2 Result Suried Ho Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic	A / / / / Dle wa A / / / / nickne	B II coppe	Minin C Pr Data(Minin C	nit:µm) num: / D (Unit:µn num: / D	E / m)		Average:	Average:
Request No. 1 2 Result Suried Ho Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic	A / / / / Dle wa A / / / / nickne	B II coppe	Minin C Pr Data(Minin C	D (Unit:μn num: / D	/ m)		Average:	Average:
No. 1 2 Result Buried Ho Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic	/ / Dole wa A / / / nickne	Il coppe	cr Data(Minin	D (Unit:μn num: /	/ m)		Average:	Average:
Result Buried Ho Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic	/ / Dole wa A / / / nickne	Il coppe	er Data(Minim	Unit:µn num: / D	/ m)		Average:	/
Result Buried Ho Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic	A / / nickne	B ess Data	Minin C	num: /	1	F	Average:	
Result Buried Ho Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Side Soldermas	A / / nickne	B ess Data	Minin C	num: /	1	F	minimum	
Request No. 1 2 Result Copper The Layer Top Side L2 L3 Bottom Sic	A / / nickne	B ess Data	Minin C	num: /	1	F	minimum	
Request No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic	A / / nickne	B ess Data	Minin C	num: /	1	F	minimum	
No. 1 2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic	/ / nickne	ess Data	С	D	E	F	minimum	
1 2 Result Copper The Layer Top Side L2 L3 Bottom Sic	/ / nickne	ess Data			/	r		Average:
2 Result Copper Thi Layer Top Side L2 L3 Bottom Sic			Unit:µı	<u></u>	/		0.0	
Result Copper Thi Layer Top Side L2 L3 Bottom Side			 (Unit:μւ		/	1		<u> </u>
Layer Top Side L2 L3 Bottom Sic			Unit:μι		,			
Layer Top Side L2 L3 Bottom Sic			Ο 1111. μ	4111				
Top Side L2 L3 Bottom Sic		l n		Fi	inish Copp	per		
L2 L3 Bottom Sic		Base	copper	Requ	uirement() stomer or)	From	Actual	Resul
L3 Bottom Sic Soldermasl	e							ACC
Bottom Sic Soldermasl								ACC
Soldermasl	ide			 			1	ACC ACC
		ckness I	———)ata(∐r	it:um)				<u> </u>
Speciment Lo				quest		A	ctual	Resul
Circuit Sur								ACC
Circuit Cor	rner							ACC
Dielectric N	Meası	ırement	Record	d (Unit:	 μm)	•		-
Layer		1		quest		A	ctual	Resul
L1-L2	1							ACC
L2-L3								ACC
L3-L4								ACC
Dispositio	on:		_					
			ACC					REJ 🗀

Solderability Test Report

General Information									
Customer Name	0	Customer P/N	0						
Product P/N	0	Sample Size	1PCS						
Remark									

Test Method: Follow IPC-TM-650 Standard

Solderability test

	Temperature	Time		Result	A agent/Deiget	Domauly
Condition		40.10.50	Blow-hole	Non-Wetting	Accept/Reject	Remark
		4S±0.5S	NO	NO	ACC	

Final Conclusion:

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Thermal Stress Test Report

General Information									
Customer Name	0	Customer P/N	0						
Product P/N	0	Sample Size	1PCS						
Remark									

Test Method: Follow IPC-TM-650 Standard

Thermal stress test

Thermal se									
	Temperature	Time			Result				A aa/Dai
Condition	2000	100 2Th	Discolor	Delamination	Measling	S/M peel off	Blister	Crack	Acc/Rej
	288°C	10S,3Times	NO	NO	NO	NO	NO	NO	ACC

Final Conclusion:

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E-Test Report

		Gene	eral Informatio	n							
Custo	mer Name	0	Customer 1		0						
Proc	duct P/N	0	Remark	ζ.							
· · · · · · · · · · · · · · · · · · ·											
Test Metho	od:										
Test Qty	CS	Pass Qty	CS	Pass	Rate	100%					
Test condit	tion										
A	Testing coverage	e rate									
В	Voltage										
C	Continuity Test										
D	Insulation Test										

This E-Test Report is to certify the electrical test has been performed per IPC-9252, Class II requirements. All product that passed testing has met and or exceeded Class II as defined by IPC-9252. Actual test method, conditions, and results are shown above.

Ionic Contamination Report

General Information						
Customer Name 0 Customer P/N 0						
Product P/N	0	Sample Size	1PCS			
PCB Description						

Test Method : Follow IPC-TM-650 Standard

Test content

Items	Data	Un	Remark	
Request				
Actual result				

Final Conclusion:

RoHS Compliance Statement

General Information						
Customer Name	0	Customer P/N	0			
Product P/N	0	PO Number				
Shipped Quantity	0	Surface Treatment	1			

Statement of Declaration

Shenzhen Global Circuits is fully aware of the need to reduce the environmental impact of our products and to meet the requirements of EU directive 2011/65/EU and Amendment 2015/863.

The above mentioned part number meet the RoHS directive as it does not exceed the allowed limits for the restricted substances listed below:

No.	Substance	Control Limit	
1	Lead (Pb)	Under 1000ppm	
2	Mercury (Hg)	Under 1000ppm	
3	Cadmium (Cd)	Under 100ppm	
4	Hexavalent Chromium	Under 1000ppm	
5	Polybrominated biphenyls (PBB)	Under 1000ppm	
6	Polybrominated diphenyl ethers (PBDE)	Under 1000ppm	
7	Bis(2-ethylhexyl) phthalate (DEHP)	Under 1000ppm	
8	Butyl benzyl phthalate (BBP)	Under 1000ppm	
9	Dibutyl phthalate (DBP)	Under 1000ppm	
10	Diisobutyl phthalate (DIBP)	Under 1000ppm	

Authorized Signature:	Date:
Printed Name:	Title:
	Final Quality Assurance Supervisor

Impedance Test Report

General Information						
Customer Name		Customer P/N				
Product P/N		Sample Size				
Test Date						

Test Result									
NO.	Request	Request Layer	Referance	Line width	Line width (mil) Line space (mil)	Actual test data			Accept/Reject
110.	(Ω)	Layer	layer	(mil)		Max(Ω)	Min(Ω)	Average(Ω)	Accept/Reject
1									ACC
2									ACC
3									ACC
4									ACC

Final Conclusion:		